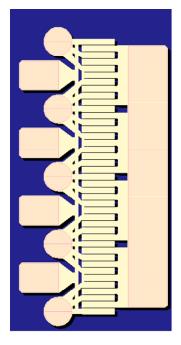


Applications

- Defense & Aerospace
- Broadband Wireless



Product Features

- Frequency Range: DC 18 GHz
- 43 dBm Nominal P_{SAT} at 3 GHz
- 78.3% Maximum PAE
- 18 dB Nominal Power Gain at 3 GHz
- Bias: $V_D = 12 32 V$, $I_{DQ} = 100 500 mA$
- Technology: TQGaN25 on SiC
- Chip Dimensions: 0.82 x 1.44 x 0.10 mm

General Description

The TriQuint TGF2023-2-05 is a discrete 5 mm GaN on SiC HEMT which operates from DC-18 GHz. The TGF2023-2-05 is designed using TriQuint's proven TQGaN25 production process. This process features advanced field plate techniques to optimize microwave power and efficiency at high drain bias operating conditions.

The TGF2023-2-05 typically provides 43.0 dBm of saturated output power with power gain of 18 dB at 3 GHz. The maximum power added efficiency is 78.3 % which makes the TGF2023-2-05 appropriate for high efficiency applications.

Lead-free and RoHS compliant

Functional Block Diagram

Pad Configuration

Pad No.	Symbol	
1-4	V _G / RF IN	
5	V _D / RF OUT	
Backside	Source / Ground	

Ordering InformationPartECCNDescriptionTGF2023-2-053A001b.3.b25 Watt GaN HEMT

Absolute Maximum Ratings

Parameter	Value
Drain to Gate Voltage (V _{DG})	100 V
Drain Voltage (V _D)	40 V
Gate Voltage Range (V _G)	–50 to 0 V
Drain Current (I _D)	5 A
Gate Current (I _G)	-5 to 14 mA
Power Dissipation (P _D)	See graph on pg.3.
CW Input Power (P _{IN})	+37 dBm
Channel Temperature (T _{CH})	275 <i>°</i> C
Mounting Temperature	320 °C
Storage Temperature	–65 to 150 <i>°</i> C

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

RF Characterization – Optimum Power Tune

Test conditions unless otherwise noted: T = 25 °C, Bond wires included. Measured data provided by Modelithics

Recommended Operating Conditions

Parameter	Value
Drain Voltage Range (V _D)	28 – 32 V
Drain Quiescent Current (I _{DQ})	250 mA
Drain Current Under RF Drive (ID)	1.5 A (Typ.)
Gate Voltage (V _G)	–3.0 V (Typ.)
Channel Temperature (T _{CH})	225℃ (Max.)

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

Parameter	Typical Value				Units
Frequency (F)			3		GHz
Drain Voltage (V _D)	12	12	28	28	V
Bias Current (I _{DQ})	100	250	100	250	mA
Input Power (P _{in})	23	23	25	25	dBm
Output Power (Pout)	39.1	39.2	43	43	dBm
Power Added Efficiency (PAE)	78.2	60.7	62.7	62.9	%
Power Gain (Gain)	16.1	16.2	18	18	dB
Parallel Resistance ⁽¹⁾ (R _p)	32.4	32.4	72.2	73.6	Ω·mm
Parallel Capacitance ⁽¹⁾ (C _p)	1.14	1.14	0.43	0.35	pF/mm
Load Reflection Coefficient $^{(2)}(\Gamma_L)$	0.84∠173°	0.84∠173°	0.65∠165°	0.62∠166°	

Notes:

1. Large signal equivalent output network (normalized).

2. Characteristic Impedance (Zo) = 50 Ω .

RF Characterization – Optimum Efficiency Tune

Test conditions unless otherwise noted: T = 25°C, Bond wires included. Measured data provided by Modelithics.

Parameter	,		al Value		Units
Frequency (F)			3		GHz
Drain Voltage (V _D)	12	12	28	28	V
Bias Current (I _{DQ})	100	250	100	250	mA
Input Power (P _{in})	23	23	25	25	dBm
Output Power (Pout)	36.5	36.6	40.3	40.4	dBm
Power Added Efficiency (PAE)	78.3	77.8	75.9	75.6	%
Power Gain (Gain)	13.5	13.6	15.3	15.4	dB
Parallel Resistance ⁽¹⁾ (R _p)	92.7	92.7	231	223	Ω·mm
Parallel Capacitance (1) (Cp)	0.82	0.82	0.55	0.54	pF/mm
Load Reflection Coefficient $^{(2)}(\Gamma_L)$	0.79∠160°	0.79∠160°	0.78∠143°	0.77∠143°	

Notes:

1. Large signal equivalent output network (normalized).

2. Characteristic Impedance (Zo) = 50 Ω .



RF Characterization – Optimum Power Tune

Test conditions unless otherwise noted: T = 25 °C, Bond wires included. Measured data provided by Modelithics.

Parameter	Typical Value				Units
Frequency (F)			6		GHz
Drain Voltage (V _D)	12	12	28	28	V
Bias Current (I _{DQ})	100	250	100	250	mA
Input Power (P _{in})	32	32	33	33	dBm
Output Power (Pout)	40.2	40.3	43.3	43.2	dBm
Power Added Efficiency (PAE)	63.0	63.1	58.4	56.5	%
Power Gain (Gain)	8.2	8.3	10.3	10.2	dB
Parallel Resistance ⁽¹⁾ (R _p)	33.2	33.2	39.3	35.3	Ω·mm
Parallel Capacitance ⁽¹⁾ (C _p)	-0.52	-0.52	0.15	0.14	pF/mm
Load Reflection Coefficient ⁽²⁾ (Γ_L)	0.83∠–173°	0.83∠–173°	0.74∠176°	0.76∠177°	

Notes:

1. Large signal equivalent output network (normalized).

2. Characteristic Impedance (Zo) = 50 Ω .

RF Characterization – Optimum Efficiency Tune

Test conditions unless otherwise noted: T = 25 °C, Bond wires included. Measured data provided by Modelithics.

Parameter	,	Typica	I Value		Units
Frequency (F)			6		GHz
Drain Voltage (V _D)	12	12	28	28	V
Bias Current (I _{DQ})	100	250	100	250	mA
Input Power (P _{in})	32	32	33	33	dBm
Output Power (Pout)	39.4	39.4	42.4	42.9	dBm
Power Added Efficiency (PAE)	69.5	69.1	74.2	73.1	%
Power Gain (Gain)	7.4	7.4	9.4	9.9	dB
Parallel Resistance ⁽¹⁾ (R _p)	23.4	23.4	57.4	47.7	Ω·mm
Parallel Capacitance ⁽¹⁾ (C _p)	-0.11	-0.11	0.55	0.57	pF/mm
Load Reflection Coefficient $^{(2)}(\Gamma_L)$	0.83∠–179°	0.83∠–179°	0.83∠167°	0.83∠169°	

Notes:

1. Large signal equivalent output network (normalized).

2. Characteristic Impedance (Zo) = 50 Ω .



RF Characterization – Optimum Power Tune

Test conditions unless otherwise noted: T = 25 °C, Bond wires included. Measured data provided by Modelithics.

Parameter	Typica	al Value		Units
Frequency (F)		10		GHz
Drain Voltage (V _D)	12	28	28	V
Bias Current (I _{DQ})	250	100	250	mA
Input Power (P _{in})	35	35	35	
Output Power (Pout)	39.1	41.7	41.9	dBm
Power Added Efficiency (PAE)	41.8	47.9	48.3	%
Power Gain (Gain)	4.1	6.7	6.9	dB
Parallel Resistance ⁽¹⁾ (R _p)	79.4	32.0	32.0	Ω·mm
Parallel Capacitance ⁽¹⁾ (C _p)	-0.29	-0.19	-0.19	pF/mm
Load Reflection Coefficient $^{(2)}(\Gamma_L)$	0.82∠–163°	0.80∠–175°	0.80∠–175°	

Notes:

1. Large signal equivalent output network (normalized).

2. Characteristic Impedance (Zo) = 50 Ω .

RF Characterization – Optimum Efficiency Tune

Test conditions unless otherwise noted: T = 25 °C, Bond wires included. Measured data provided by Modelithics.

Parameter	Typic	al Value		Units
Frequency (F)		10		GHz
Drain Voltage (V _D)	12	28	28	V
Bias Current (I _{DQ})	250	100	250	mA
Input Power (P _{in})	35	35	35	
Output Power (Pout)	39.0	41.6	41.8	dBm
Power Added Efficiency (PAE)	42.4	48.9	49.2	%
Power Gain (Gain)	4.0	6.6	6.8	dB
Parallel Resistance ⁽¹⁾ (R _p)	66.4	29.3	30.5	Ω·mm
Parallel Capacitance ⁽¹⁾ (C _p)	-0.29	-0.13	-0.16	pF/mm
Load Reflection Coefficient ⁽²⁾ (Γ _L)	0.81∠–165°	0.80∠–177°	0.80∠–176°	

Notes:

1. Large signal equivalent output network (normalized).

2. Characteristic Impedance (Zo) = 50 Ω .

TriQuint 🕥

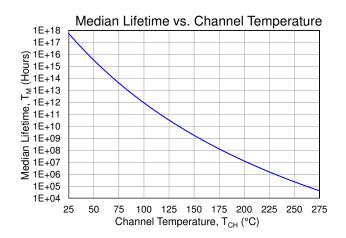
Thermal and Reliability Information (1)

Parameter	Test Conditions	Value	Units
Thermal Resistance, θ_{JC} (No RF Drive)		5.79	°C/W
Channel Temperature, T _{CH} (No RF Drive)	V _D = 28 V, I _D = 0.5 A , P _D = 14 W, Tbaseplate = 70 ℃	151	C
Median Lifetime, T _M (No RF Drive)	1	1.57 x 10^9	Hrs
Thermal Resistance, θ_{JC} (Under RF Drive)	$V_{\rm D} = 28 \text{ V}, I_{\rm D} = 1.54 \text{ mA},$	6.09	°C/W
Channel Temperature, T _{CH} (Under RF Drive)	$P_{OUT} = 43.9 \text{ dBm}, P_D = 18 \text{ W},$	180	°C
Median Lifetime, T_M (Under RF Drive)	Tbaseplate = 70 ℃	7.99 x 10^7	Hrs

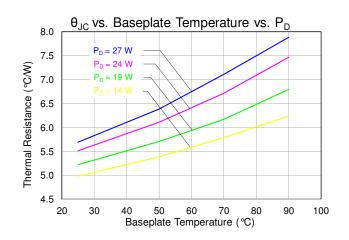
Notes:

1. Assumes eutectic attach using 1mil thick 80/20 AuSn mounted to a 10 mil CuMo Carrier Plate.

Median Lifetime

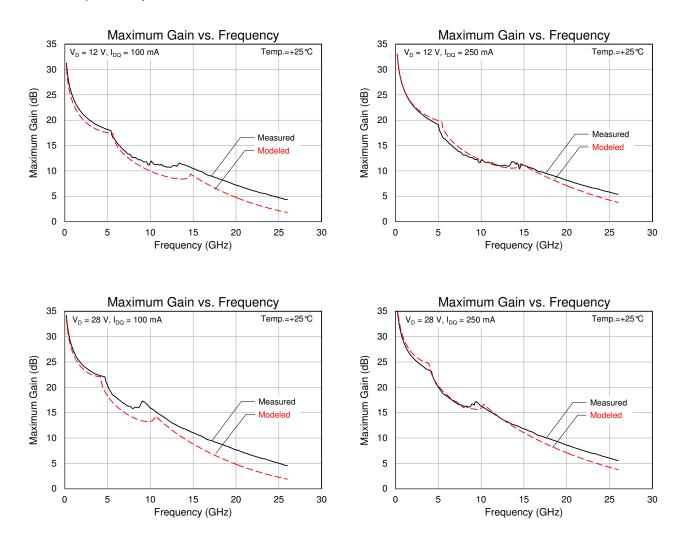


Thermal Resistance



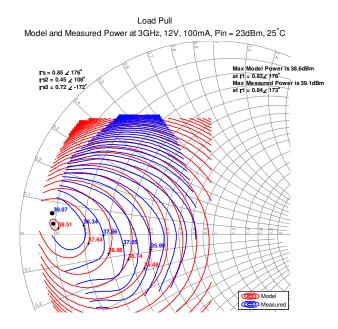


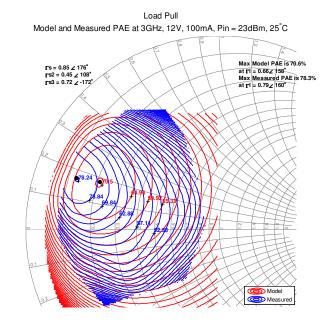
Maximum Gain Performance



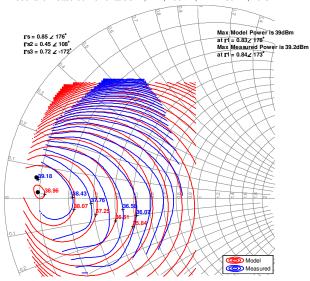


Load Pull Contours

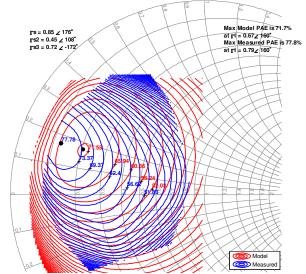




Load Pull Model and Measured Power at 3GHz, 12V, 100mA, Pin = 23dBm, 25°C

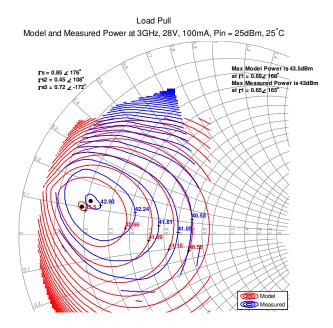


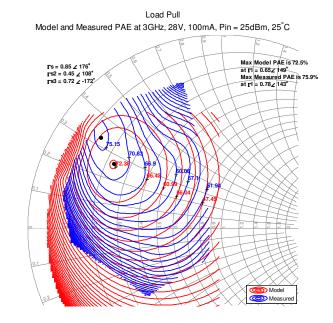
Load Pull Model and Measured PAE at 3GHz, 12V, 100mA, Pin = 23dBm, 25°C



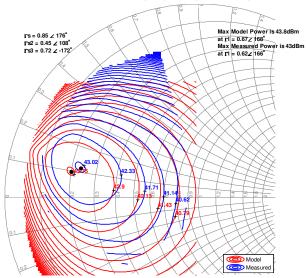


Load Pull Contours

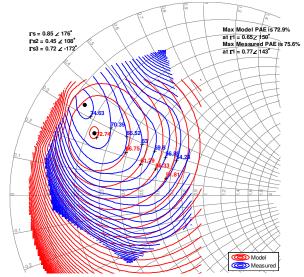




Load Pull Model and Measured Power at 3GHz, 28V, 250mA, Pin = 25dBm, 25°C

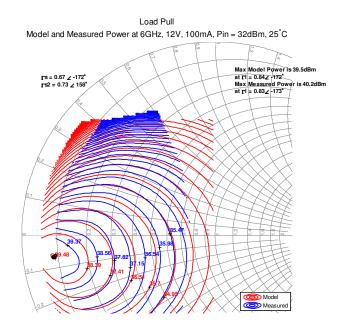


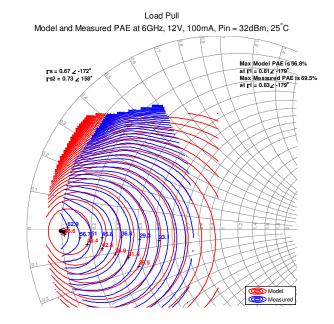
 $\label{eq:Load Pull} Load Pull \\ \mbox{Model and Measured PAE at 3GHz, 28V, 250mA, Pin = 25dBm, 25 °C } \\$



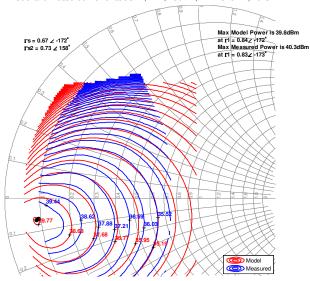


Load Pull Contours

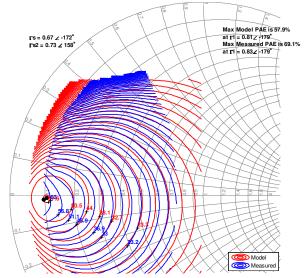




Load Pull Model and Measured Power at 6GHz, 12V, 250mA, Pin = 32dBm, 25°C

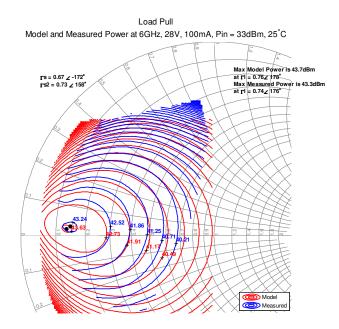


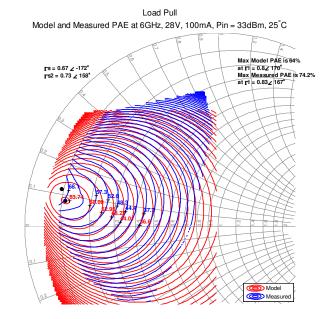
 $\label{eq:load_pull} \mbox{Load Pull}$ Model and Measured PAE at 6GHz, 12V, 250mA, Pin = 32dBm, 25 $^\circ \mbox{C}$



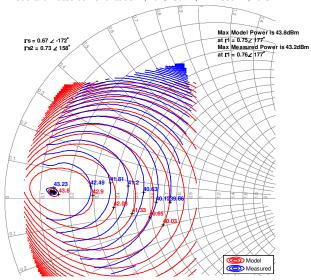


Load Pull Contours

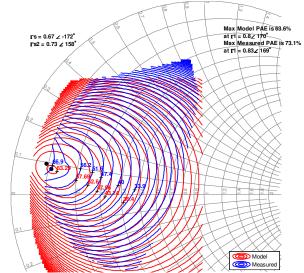




Load Pull Model and Measured Power at 6GHz, 28V, 250mA, Pin = 33dBm, 25°C

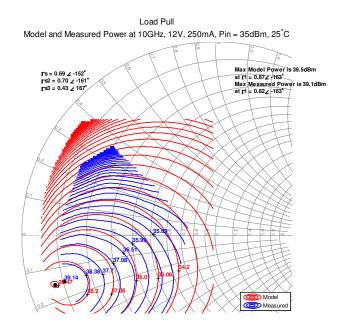


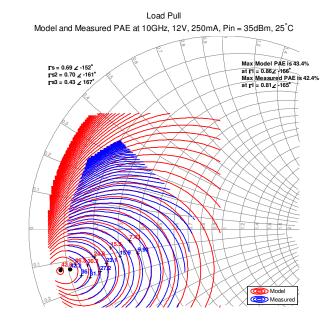
 $\label{eq:Load Pull} Load Pull $$Model and Measured PAE at 6GHz, 28V, 250mA, Pin = 33dBm, 25 ^{\circ}C$$$



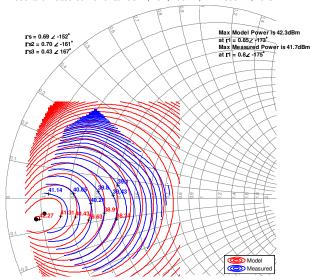


Load Pull Contours

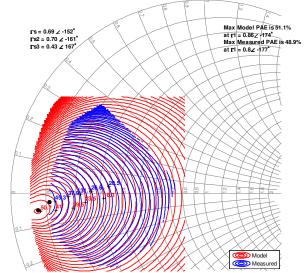




Load Pull Model and Measured Power at 10GHz, 28V, 100mA, Pin = 35dBm, 25°C

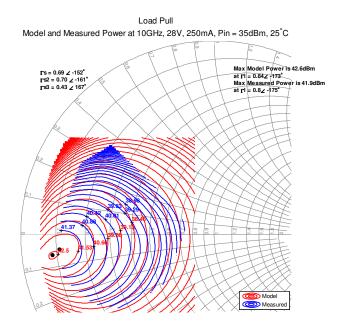


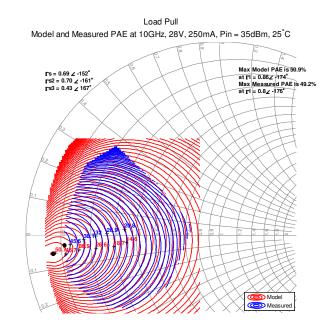
Load Pull Model and Measured PAE at 10GHz, 28V, 100mA, Pin = 35dBm, 25°C





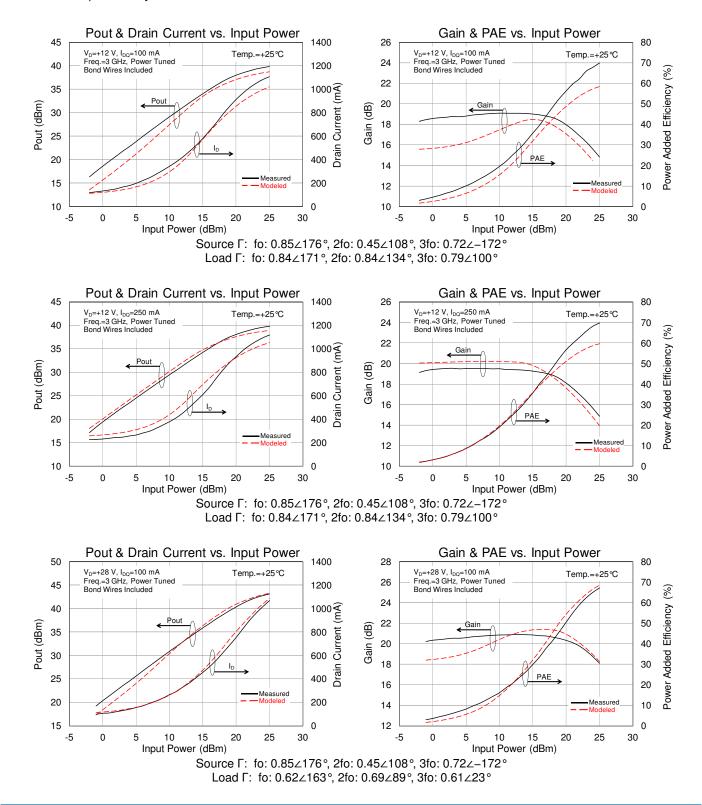
Load Pull Contours





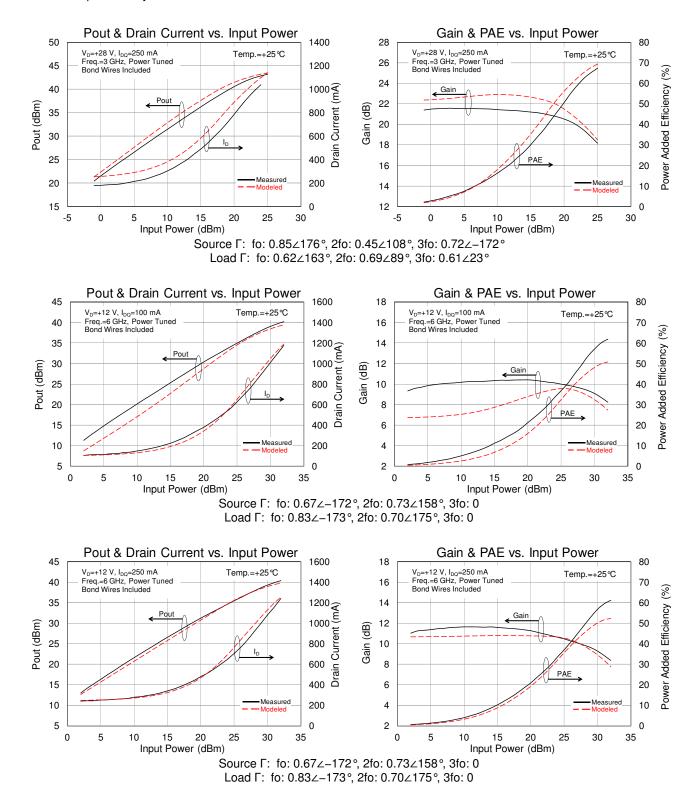


Power Tuned Data



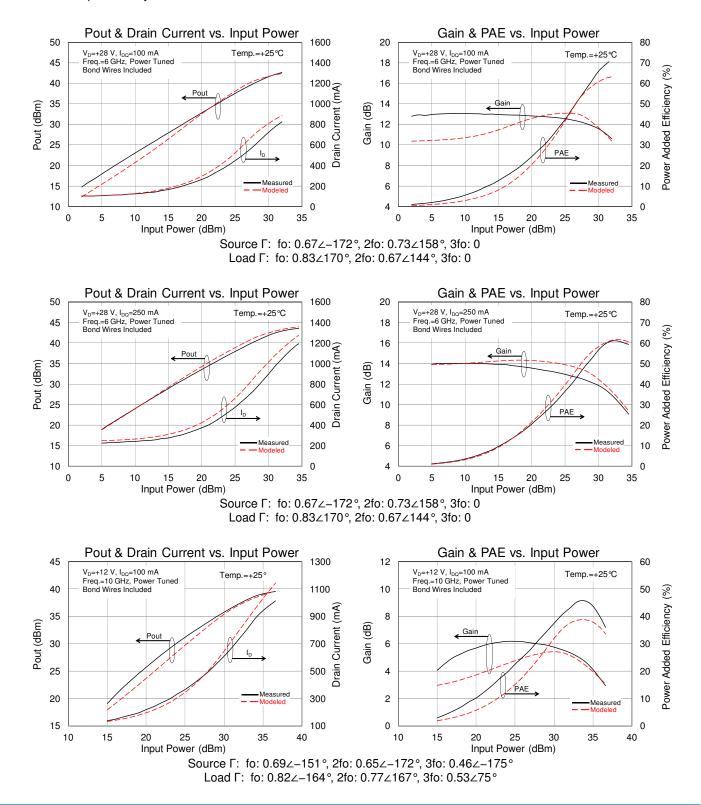


Power Tuned Data



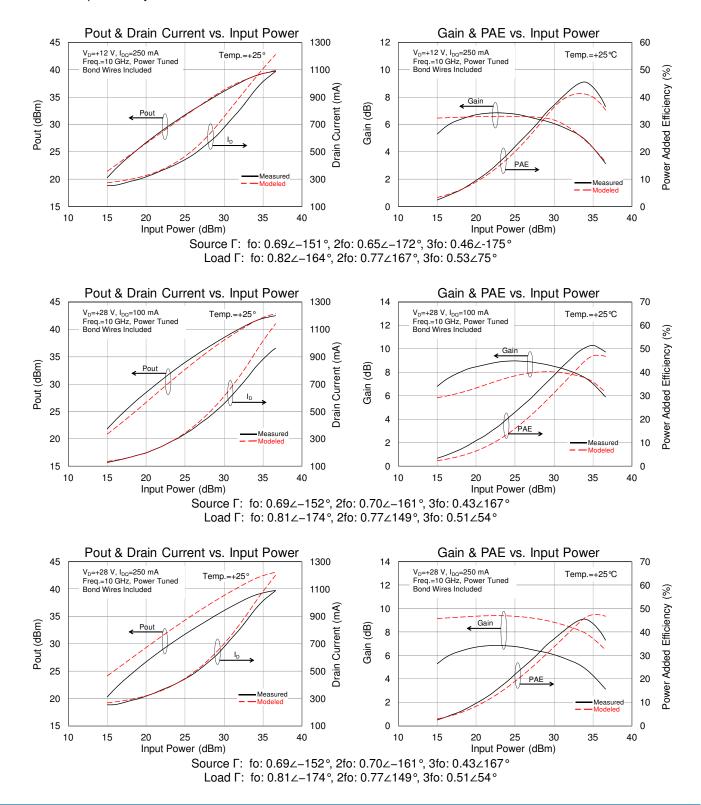


Power Tuned Data





Power Tuned Data

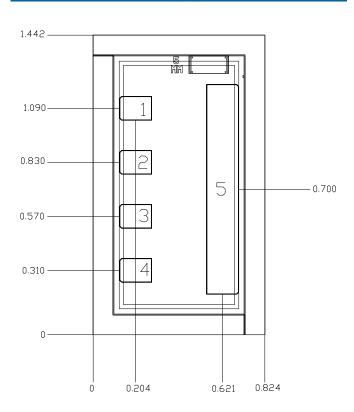


TriQuint 1

Model

A non-linear model is available for download from Modelithics (at <u>http://www.modelithics.com/mvp/Triquint&tab=3</u>) by approved TriQuint customers. The model is compatible with the industry's most popular design software including Agilent ADS and National Instruments/AWR applications. Once on the Modelithics web page, the user will need to register for a free license before being granted the download.

Mechanical Drawing



Bond Pads

Pad No.	Description	Dimensions
1-4	Gate	0.154 x 0.115
5	Drain	0.154 x 1.01
Die Backside	Source / Ground	0.824 x 1.442

Notes:

- 1. Units: millimeters
- 2. Thickness: 0.100 mm
- 3. Die x,y size tolerance: +/- 0.050 mm



Assembly Notes

Component placement and adhesive attachment assembly notes:

- Vacuum pencils and/or vacuum collets are the preferred method of pick up.
- Air bridges must be avoided during placement.
- The force impact is critical during auto placement.
- Organic attachment (i.e. epoxy) not recommended.

Reflow process assembly notes:

- Use AuSn (80/20) solder and limit exposure to temperatures above 300°C to 3-4 minutes, maximum.
- An alloy station or conveyor furnace with reducing atmosphere should be used.
- Do not use any kind of flux.
- Coefficient of thermal expansion matching is critical for long-term reliability.
- Devices must be stored in a dry nitrogen atmosphere.

Interconnect process assembly notes:

- Ball bonding is the preferred interconnect technique, except where noted on the assembly diagram.
- Force, time, and ultrasonics are critical bonding parameters.
- Aluminum wire should not be used.
- Devices with small pad sizes should be bonded with 0.0007-inch wire.

Disclaimer

GaN/SiC devices are susceptible to damage from Electrostatic Discharge. Proper precautions should be observed during handling, assembly and test.

Bias-up Procedure

- 1. V_G set to -5 V.
- 2. V_D set to 28 V.
- 3. Adjust V_G more positive until quiescent I_D is 500 mA.
- 4. Apply RF signal.

Bias-down Procedure

- 1. Turn off RF signal.
- 2. Turn off V_{D} and wait 1 second to allow drain capacitor dissipation.
- 3. Turn off V_G .



Product Compliance Information

ESD Sensitivity Ratings



Caution! ESD-Sensitive Device

ESD Rating: TBD Value: TBD Test: TBD Standard: TBD

Solderability

Compatible with gold/tin (320°C maximum reflow temperature) soldering processes.

RoHs Compliance

This part is compliant with EU 2002/95/EC RoHS directive (Restrictions on the Use of Certain Hazardous Substances in Electrical and Electronic Equipment).

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A (C₁₅H₁₂Br₄0₂) Free
- PFOS Free
- SVHC Free

Contact Information

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